

## 500V, 8.0A, 0.72Ω N-channel Power Planar MOSFET

### JMPK840BJ

#### Features

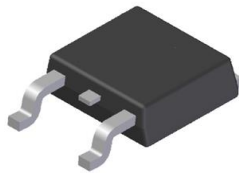
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS Tested
- 100%  $\Delta V_{ds}$  Tested
- Halogen-free; RoHS-compliant

#### Applications

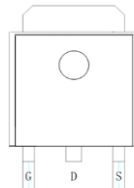
- Load Switch
- PWM Application
- Power Management

#### Product Summary

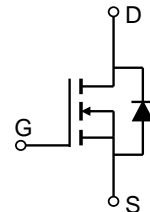
Parameters	Value	Unit
$V_{DSS}$	500	V
$V_{GS(th)}_{Typ}$	3.1	V
$I_D(@V_{GS}=10V)$	8.0	A
$R_{DS(ON)}_{Typ}(@V_{GS}=10V)$	0.72	$\Omega$



TO-252-3L



Pin Assignment



Schematic Diagram

#### Ordering Information

Device	Marking	MSL	Form	Package	Reel(pcs)	Per Carton (pcs)
JMPK840BJ	JMPK840BJ	3	Tape&Reel	TO-252-3L	2500	25000

#### Absolute Maximum Ratings (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-to-Source Voltage	500	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 30$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	8.0
		$T_C = 100^\circ\text{C}$	5.1
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	Refer to Fig.4	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	245	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ\text{C}$	132
		$T_C = 100^\circ\text{C}$	53
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$

#### Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(3)</sup>	37	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.0	

**Electrical Characteristics** ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}$ , $V_{GS} = 0\text{V}$	500	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 500\text{V}$ , $V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}$ , $V_{GS} = \pm 30\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250\mu\text{A}$	2.0	3.1	4.0	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(4)</sup>	$V_{GS} = 10\text{V}$ , $I_D = 1\text{A}$	-	721	902	m $\Omega$
<b>Dynamic Characteristics</b>						
$R_g$	Gate Resistance	$f = 1\text{MHz}$	-	2.2	-	$\Omega$
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}$ , $V_{DS} = 250\text{V}$ , $f = 1\text{MHz}$	-	1133	1699	pF
$C_{oss}$	Output Capacitance		-	40	60	pF
$C_{rss}$	Reverse Transfer Capacitance		-	8.6	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0$ to $10\text{V}$ $V_{DS} = 250\text{V}$ , $I_D = 1\text{A}$	-	24	-	nC
$Q_{gs}$	Gate Source Charge		-	5.7	-	nC
$Q_{gd}$	Gate Drain ("Miller") Charge		-	7.0	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}$ , $V_{DD} = 250\text{V}$ $I_D = 1\text{A}$ , $R_{GEN} = 24\Omega$	-	20	-	ns
$t_r$	Turn-On Rise Time		-	19	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	83	-	ns
$t_f$	Turn-Off Fall Time		-	44	-	ns
<b>Body Diode Characteristics</b>						
$I_S$	Maximum Continuous Body Diode Forward Current		-	-	8.0	A
$I_{SM}$	Maximum Pulsed Body Diode Forward Current		-	-	32	A
$V_{SD}$	Body Diode Forward Voltage	$V_{GS} = 0\text{V}$ , $I_S = 1\text{A}$	-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = 1\text{A}$ , $di/dt = 100\text{A}/\mu\text{s}$	82	164	246	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	704	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $E_{AS}$  condition: Starting  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = 100\text{V}$ ,  $V_{GS} = 10\text{V}$ ,  $R_G = 25\text{ohm}$ ,  $L = 10\text{mH}$ ,  $I_{AS} = 7\text{A}$ ,  $V_{DD} = 0\text{V}$  during time in avalanche.
  3.  $R_{\theta JA}$  is measured with the device mounted on a  $1\text{inch}^2$  pad of 2oz copper FR4 PCB.
  4. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .



## Typical Performance Characteristics

Figure 1: Power De-rating

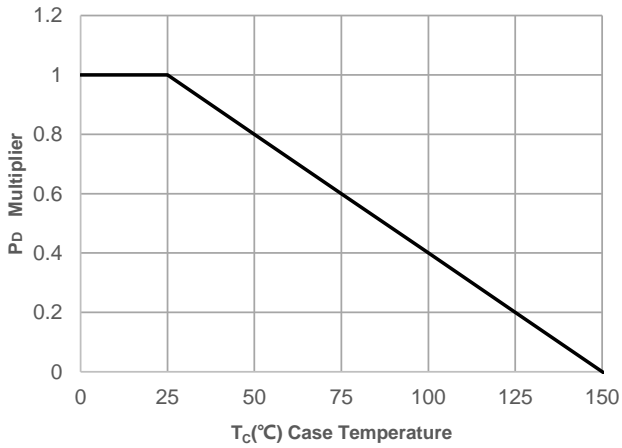


Figure 2: Current De-rating

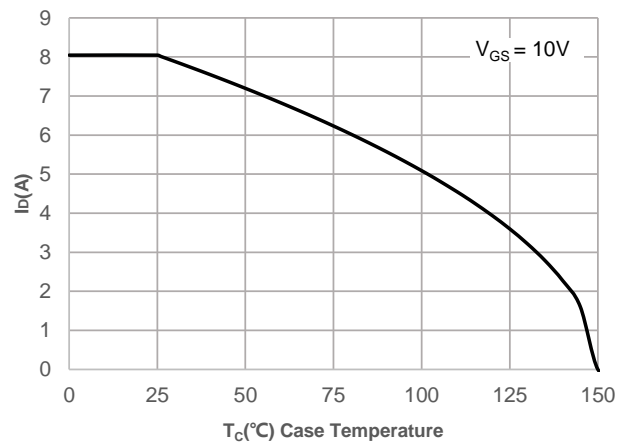


Figure 3: Normalized Maximum Transient Thermal Impedance

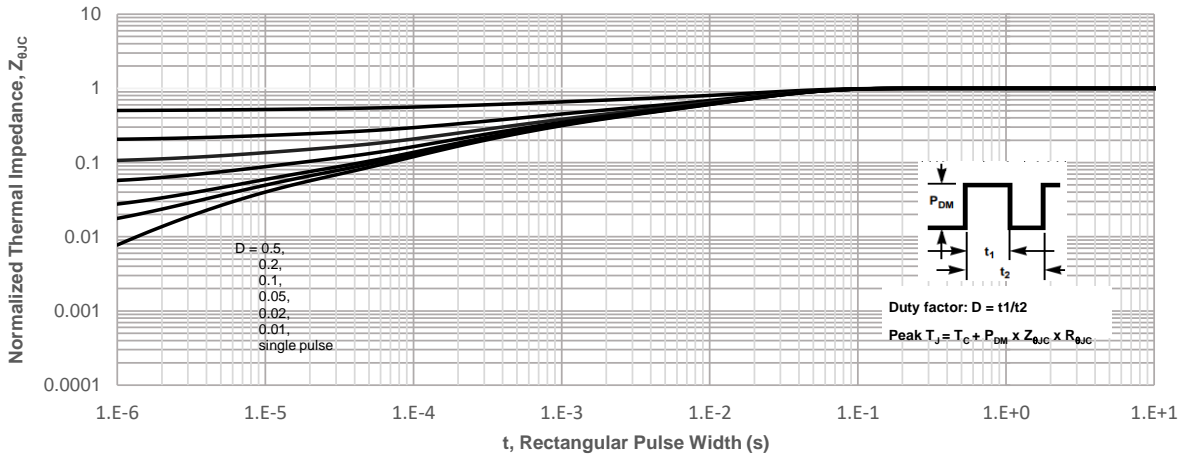
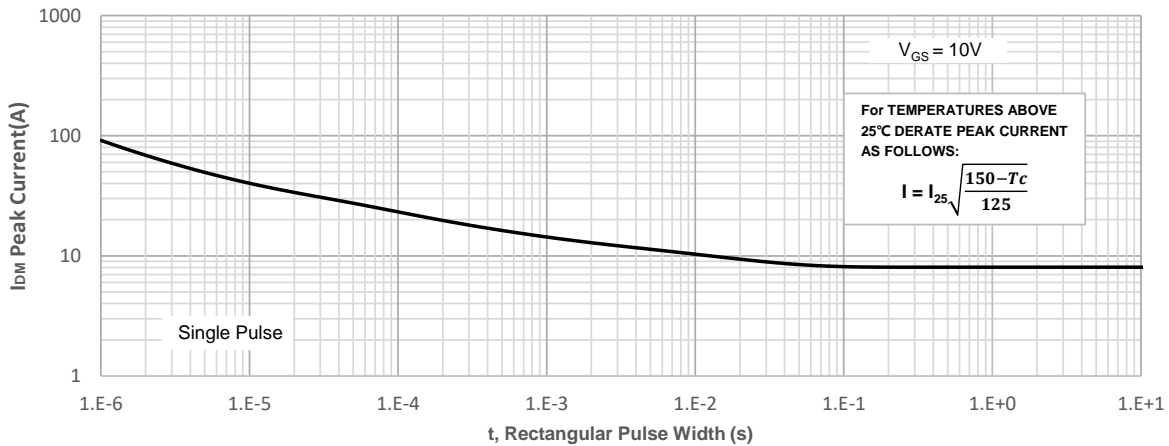
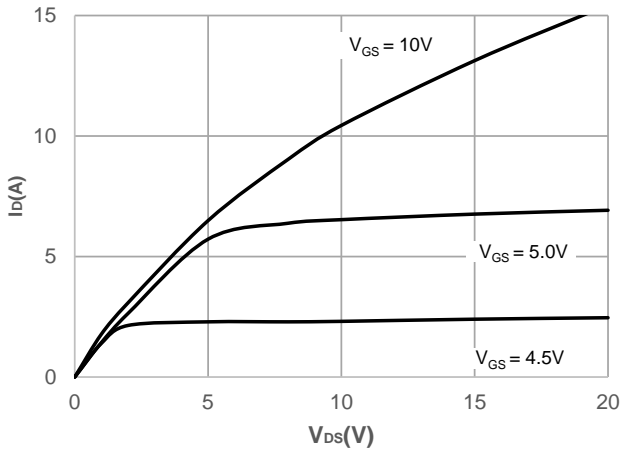
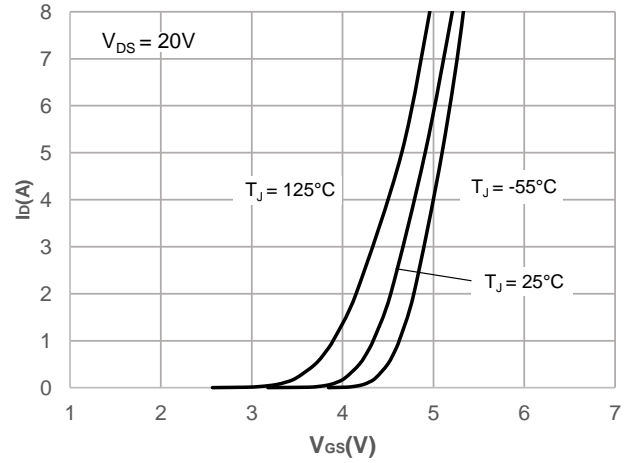
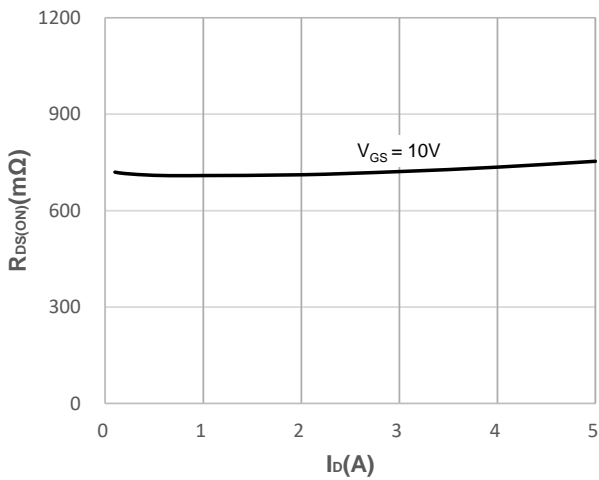
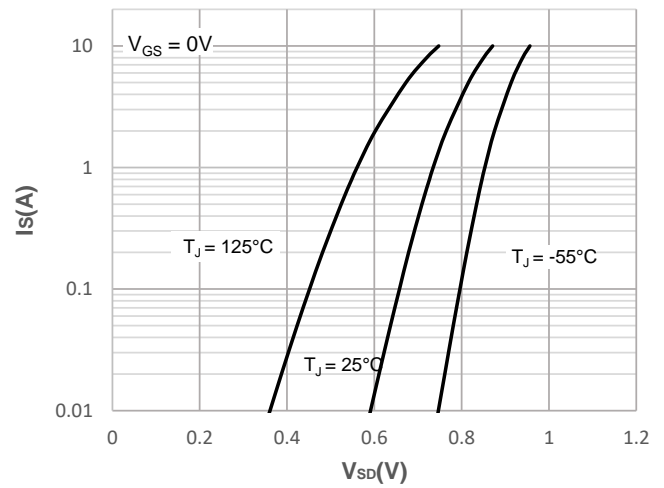
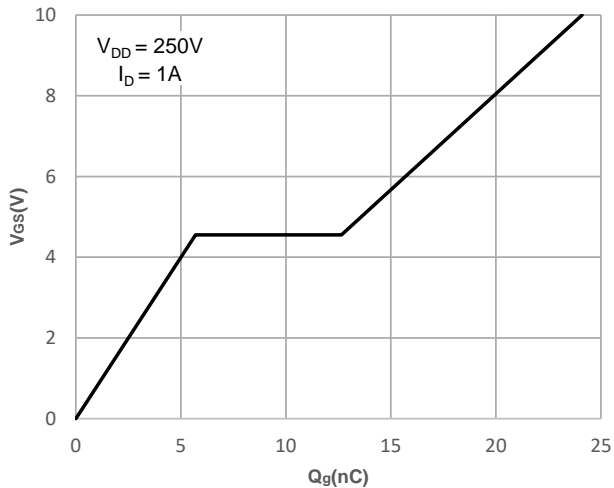
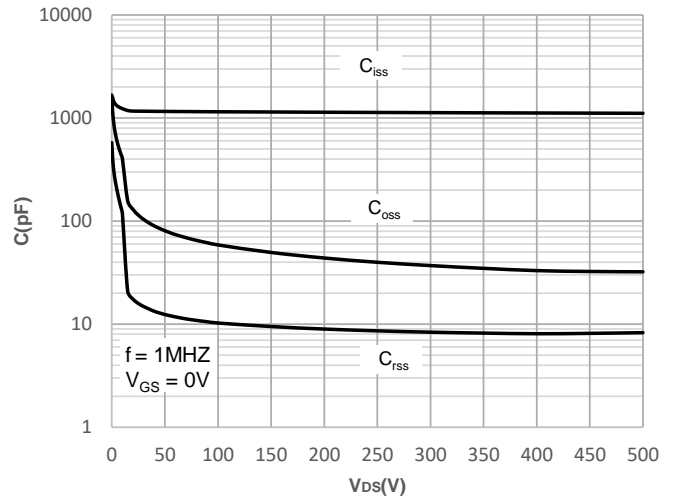


Figure 4: Peak Current Capacity

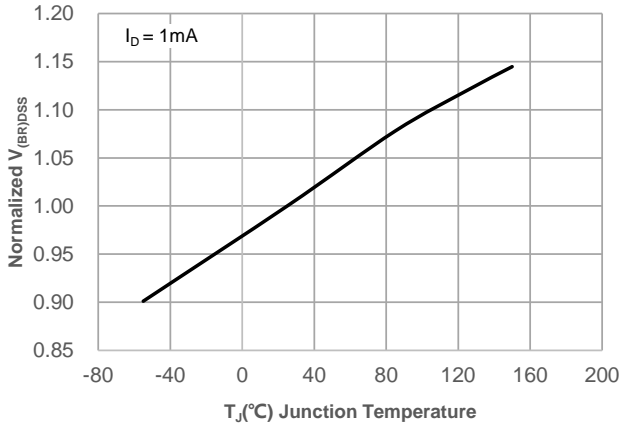


## Typical Performance Characteristics

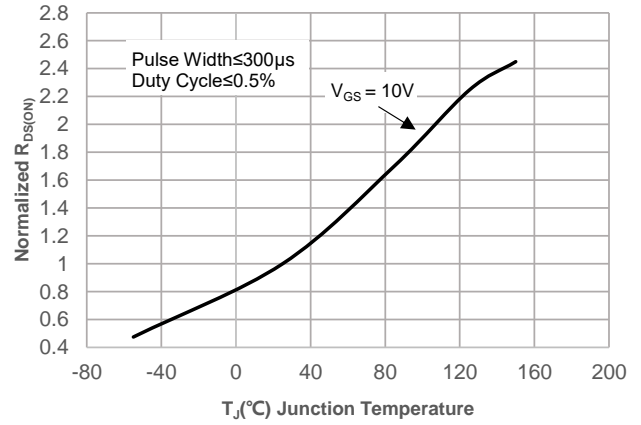
**Figure 5: Output Characteristics**

**Figure 6: Typical Transfer Characteristics**

**Figure 7: On-resistance vs. Drain Current**

**Figure 8: Body Diode Characteristics**

**Figure 9: Gate Charge Characteristics**

**Figure 10: Capacitance Characteristics**


## Typical Performance Characteristics

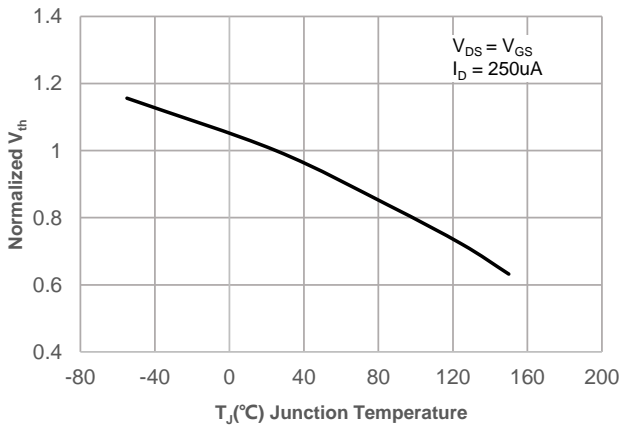
**Figure 11: Normalized Breakdown voltage vs. Junction Temperature**



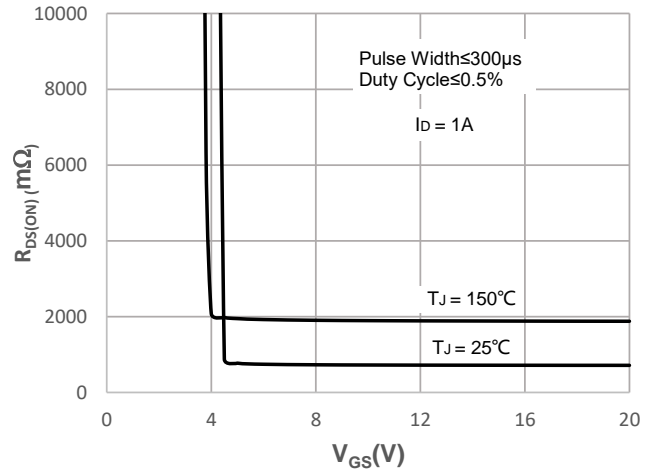
**Figure 12: Normalized on Resistance vs. Junction Temperature**



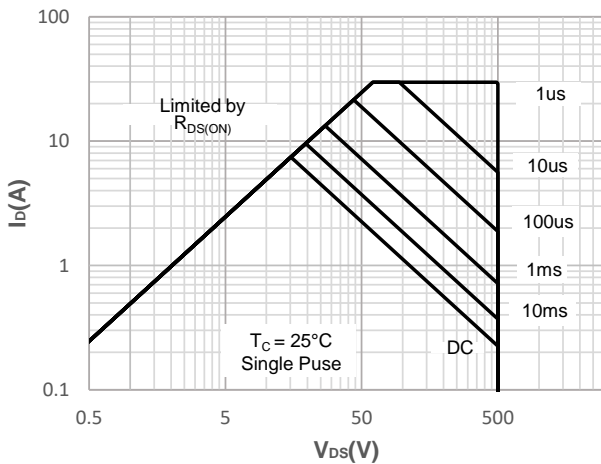
**Figure 13: Normalized Threshold Voltage vs. Junction Temperature**

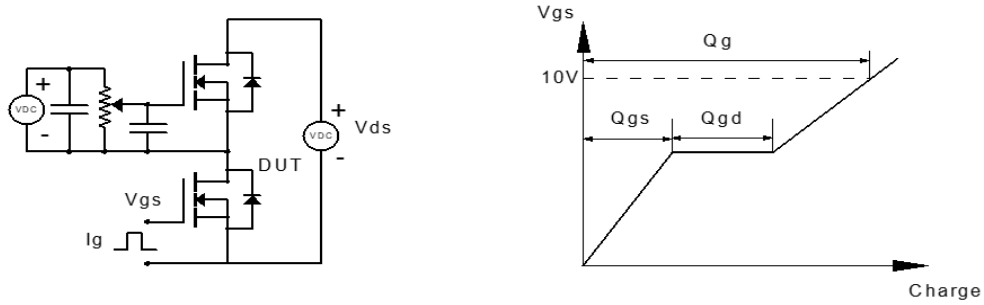
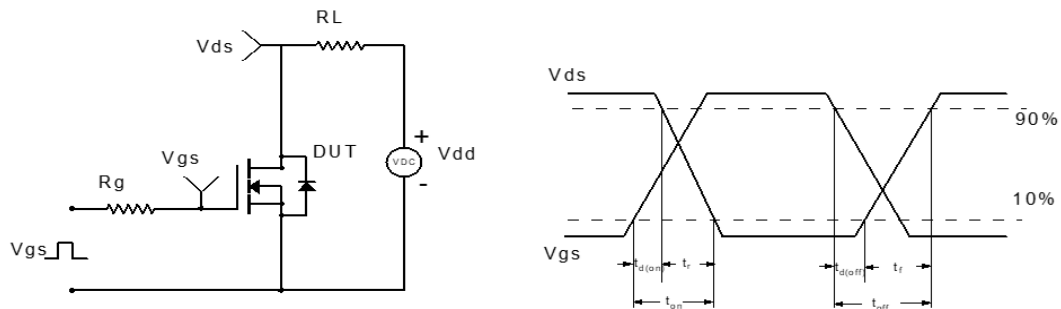


**Figure 14:  $R_{DS(ON)}$  vs.  $V_{GS}$**



**Figure 15: Maximum Safe Operating Area**



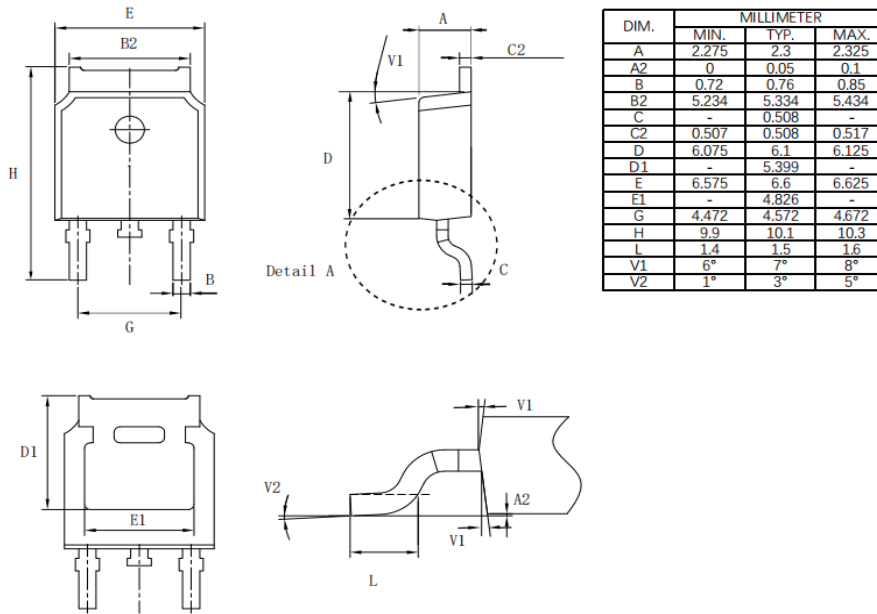
**Test Circuit**

**Figure 1: Gate Charge Test Circuit & Waveform**

**Figure 2: Resistive Switching Test Circuit & Waveform**

**Figure 3: Unclamped Inductive Switching Test Circuit & Waveform**

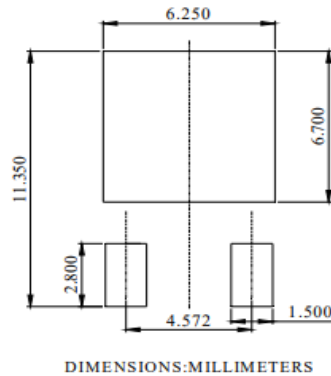
**Figure 4: Diode Recovery Test Circuit & Waveform**


## Package Mechanical Data(TO-252-3L)

### Package Outline



### Recommended Soldering Footprint



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